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second phase angle, respectively, by searching, in parallel, N/2 chips, the first long code and

the second long code.

16. The method as set forth in claim 15, with the step of transmitting including the

step of transmitting the first long code and the second long code, at an in-phase (I) angle and

at a quadrature-phase angle, respectively, on the carrier signal, with the I-phase angle ninety

degrees out of phase with the Q-phase angle.

17. The method as set forth in claim 16, with the step of acquiring including the

step of acquiring, from the communications channel, using an I-phase acquisition circuit and

a Q-phase acquisition circuit in parallel, the first long code and the second long code from

the I-phase angle and the Q-phase angle, respectively, of the carrier signal by searching, in

parallel, N/2 chips, the first long code and the second long code.

18. The method as set forth in claim 15, with the step of transmitting including the

step of transmitting the first long code and the second long code, at an in-phase (I) angle and

at a quadrature-phase (Q) angle, respectively, on the carrier signal, with the I-phase angle

ninety degrees out of phase with the Q-phase angle.

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19. The method as set forth in claim 18, with the step of acquiring including the step of acquiring, from the communications channel, using an I-phase acquisition circuit and a Q-phase acquisition circuit in parallel, the first long code and the second long code from the I-phase angle and the Q-phase angle, respectively, of the carrier signal by searching, in parallel, N/2 chips, the first long code and the second long code.

- 20. The method as set forth in claim 15, with the step of generating including the step of generating the first long code and the second long code, from a multiplicity of short codes, with each short code different from other short code in the multiplicity of short codes, and each short code in the multiplicity of short codes having a length less than N chips, with a first short code thereby embedded in the first long code, and a second short code embedded in the second long code.
- 21. The method as set forth in claim 20, with the step of acquiring including the step of acquiring, from the communications channel, using the two phase-acquisition circuits in parallel, the first short code embedded in the first long code, and the second short code embedded in the second long code, from the first phase angle and the second phase angle, respectively, by searching, in parallel, N/2 chips, the first short code and the second short code.

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A method for rapidly acquiring a spreading code, used in a code division 22. multiple access (CDMA) system, the method comprising the steps of:

generating a plurality of P long codes, where P is a number of long codes in the plurality of long codes, with each long code having a length N chips, with each long code different from other long codes in the plurality of long codes;

transmitting, over a communications channel using radio waves, the plurality of long codes at a plurality of phase angles, respectively, on a carrier signal, with each phase angle in the plurality of phase angles different from other phase angles in the plurality of phase angles; and

acquiring, from the communications channel, using phase-acquisition means, the plurality of long codes, respectively, by searching, in parallel, N/P chips, each long code of the plurality of long codes.

The method as set forth in claim 22, with the step of transmitting including the 23. step of transmitting the plurality of long codes, at the plurality of phase angles.

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24. The method as set forth in claim 23, with the step of acquiring including the step of acquiring, from the communications channel, using the phase-acquisition means, the plurality of long codes from the plurality of phase angles, respectively, of the carrier signal by searching, in parallel, N/P chips, of each of the plurality of long codes.

- 25. The method as set forth in claim 23, with the step of transmitting including the step of transmitting the plurality of long codes at the plurality of phase angles, respectively, on the carrier signal, with each phase angle in the plurality of phase angles representing an M-ary phase scheme.
- 26. The method as set forth in claim 25, with the step of acquiring including the step of acquiring, from the communications channel, using the phase-acquisition means, the plurality of long codes, respectively, of the carrier signal by searching, in parallel, N/P chips, of the first long code and the second long code.
- 27. The method as set forth in claim 23, with the step of generating including the step of generating the plurality of long codes, from a multiplicity of short codes, with each short code different from other short code in the multiplicity of short codes, and each short code in the multiplicity of short codes having a length less than N chips, with a first short

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5 code of the short codes multiplicity thereby embedded in a first long code of the long codes

multiplicity, and a second short code of the short codes multiplicity embedded in a second

long code of the long codes multiplicity.

28. The method as set forth in claim 27, with the step of acquiring including the

step of acquiring, from the communications channel, using the phase-acquisition means, the

first short code embedded in the first long code, and the second short code embedded in the

second long code, from the first phase angle and the second phase angle, respectively, by

searching, in parallel, N/P chips, the first short code and the second short code.

29. A system for rapidly acquiring a spreading code, used in a code division

multiple access (CDMA) system, the system comprising:

generator means for generating a first long code and a second long code, with each

long code having a length N chips, with the first long code different from the second long

code;

transmitter means for transmitting, over a communications channel using radio waves,

the first long code and the second long code, at a first phase angle and at a second phase

angle, respectively, on a carrier signal, with the first phase angle different from the second

phase angle; and

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acquisition means for acquiring, from the communications channel, using two phase-acquisition circuits in parallel, the first long code and the second long code from the first phase angle and the second phase angle, respectively, by searching, in parallel, N/2 chips, the first long code and the second long code.

30. The system as set forth in claim 29, with said transmitter means further

including means for transmitting the first long code and the second long code, at an in-phase

(I) angle and at a quadrature-phase angle, respectively, on the carrier signal, with the I-phase

angle ninety degrees out of phase with the Q-phase angle.

31. The system as set forth in claim 30, with said acquisition means further

including means for acquiring, from the communications channel, using an I-phase

acquisition circuit and a Q-phase acquisition circuit in parallel, the first long code and the

second long code from the I-phase angle and the Q-phase angle, respectively, of the carrier

signal by searching, in parallel, N/2 chips, the first long code and the second long code.

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32. The system as set forth in claim 29, with said transmitter means further

including means for transmitting the first long code and the second long code, at an in-phase

(I) angle and at a quadrature-phase (Q) angle, respectively, on the carrier signal, with the I-

phase angle ninety degrees out of phase with the Q-phase angle.

33. The system as set forth in claim 32, with said acquisition means further

including means for acquiring, from the communications channel, using an I-phase

acquisition circuit and a Q-phase acquisition circuit in parallel, the first long code and the

second long code from the I-phase angle and the Q-phase angle, respectively, of the carrier

signal by searching, in parallel, N/2 chips, the first long code and the second long code.

34. The system as set forth in claim 29, with said generator means further

including means for generating the first long code and the second long code, from a

multiplicity of short codes, with each short code different from other short code in the

multiplicity of short codes, and each short code in the multiplicity of short codes having a

length less than N chips, with a first short code thereby embedded in the first long code, and

a second short code embedded in the second long code.

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35. The system as set forth in claim 34, with said acquisition means further including means for acquiring, from the communications channel, using the two phase-acquisition circuits in parallel, the first short code embedded in the first long code, and the second short code embedded in the second long code, from the first phase angle and the second phase angle, respectively, by searching, in parallel, N/2 chips, the first short code and the second short code.

36. A method for rapidly acquiring a spreading code, used in a code division multiple access (CDMA) system, the method comprising the steps of:

generator means for generating a plurality of P long codes, where P is a number of long codes in the plurality of long codes, with each long code having a length N chips, with each long code different from other long codes in the plurality of long codes;

transmitter means for transmitting, over a communications channel using radio waves, the plurality of long codes at a plurality of phase angles, respectively, on a carrier signal, with each phase angle in the plurality of phase angles different from other phase angles in the plurality of phase angles; and

acquisition means for acquiring, from the communications channel, using phase-acquisition means, the plurality of long codes, respectively, by searching, in parallel, N/P chips, each long code of the plurality of long codes.

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37. The method as set forth in claim 36, with said transmitter means further including means for transmitting the plurality of long codes, at the plurality of phase angles.

38. The method as set forth in claim 37, with said acquisition means further including means for acquiring, from the communications channel, using the phase-acquisition means, the plurality of long codes from the plurality of phase angles, respectively, of the carrier signal by searching, in parallel, N/P chips, of each of the plurality of long codes.

- 39. The method as set forth in claim 36, with said transmitter means further including means for transmitting the plurality of long codes at the plurality of phase angles, respectively, on the carrier signal, with each phase angle in the plurality of phase angles representing an M-ary phase scheme.
- 40. The method as set forth in claim 39, with said acquisition means further including means for acquiring, from the communications channel, using the phase-acquisition means, the plurality of long codes, respectively, of the carrier signal by searching, in parallel, N/P chips, of the first long code and the second long code.

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41. The method as set forth in claim 36, with said generator means further including means for generating the plurality of long codes, from a multiplicity of short codes, with each short code different from other short code in the multiplicity of short codes, and each short code in the multiplicity of short codes having a length less than N chips, with a first short code of the short codes multiplicity thereby embedded in a first long code of the long codes multiplicity, and a second short code of the short codes multiplicity embedded in a second long code of the long codes multiplicity.

42. The method as set forth in claim 41, with said acquisition means further including means for acquiring, from the communications channel, using the phase-acquisition means, the first short code of the short codes multiplicity embedded in the first long code of the long codes multiplicity, and the second short code of the short codes multiplicity embedded in the second long code of the long codes multiplicity, from the first phase angle and the second phase angle, respectively, by searching, in parallel, N/P chips, the first short code and the second short code.

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43. A system for rapidly acquiring a spreading code, used in a code division

multiple access (CDMA) system, the system comprising:

a code generator for generating a first long code and a second long code, with each

long code having a length N chips, with the first long code different from the second long

code;

a transmitter, coupled to said code generator, for transmitting, over a communications

channel using radio waves, the first long code and the second long code, at a first phase angle

and at a second phase angle, respectively, on a carrier signal, with the first phase angle

different from the second phase angle; and

an acquisition circuit, coupled to the communications channel, for acquiring, from the

communications channel, using two phase-acquisition circuits in parallel, the first long code

and the second long code from the first phase angle and the second phase angle, respectively,

by searching, in parallel, N/2 chips, the first long code and the second long code.

44. The system as set forth in claim 43, with said transmitter further including

means for transmitting the first long code and the second long code, at an in-phase (I) angle

and at a quadrature-phase angle, respectively, on the carrier signal, with the I-phase angle

ninety degrees out of phase with the Q-phase angle.

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45. The system as set forth in claim 44, with said acquisition circuit further including means for acquiring, from the communications channel, using an I-phase acquisition circuit and a Q-phase acquisition circuit in parallel, the first long code and the second long code from the I-phase angle and the Q-phase angle, respectively, of the carrier

signal by searching, in parallel, N/2 chips, the first long code and the second long code.

46. The system as set forth in claim 43, with said transmitter further including

means for transmitting the first long code and the second long code, at an in-phase (I) angle

and at a quadrature-phase (Q) angle, respectively, on the carrier signal, with the I-phase

angle ninety degrees out of phase with the Q-phase angle.

47. The system as set forth in claim 46, with said acquisition circuit further

including means for acquiring, from the communications channel, using an I-phase

acquisition circuit and a Q-phase acquisition circuit in parallel, the first long code and the

second long code from the I-phase angle and the Q-phase angle, respectively, of the carrier

signal by searching, in parallel, N/2 chips, the first long code and the second long code.

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48. The system as set forth in claim 43, with said code generator further including

means for generating the first long code and the second long code, from a multiplicity of

short codes, with each short code different from other short code in the multiplicity of short

codes, and each short code in the multiplicity of short codes having a length less than N

chips, with a first short code thereby embedded in the first long code, and a second short

code embedded in the second long code.

49. The system as set forth in claim 48, with said acquisition circuit further

including means for acquiring, from the communications channel, using the phase-

acquisition circuit, the first short code embedded in the first long code, and the second short

code embedded in the second long code, from the first phase angle and the second phase

angle, respectively, by searching, in parallel, N/2 chips, the first short code and the second

short code.

50. A system for rapidly acquiring a spreading code, used in a code division

multiple access (CDMA) system, the system comprising the steps of:

a code generator for generating a plurality of P long codes, where P is a number of

long codes in the plurality of long codes, with each long code having a length N chips, with

each long code different from other long codes in the plurality of long codes;

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a transmitter, coupled to said code generator, for transmitting, over a communications

channel using radio waves, the plurality of long codes at a plurality of phase angles,

respectively, on a carrier signal, with each phase angle in the plurality of phase angles

different from other phase angles in the plurality of phase angles; and

an acquisition circuit, coupled to the communications channel, for acquiring, from the

communications channel, using said phase-acquisition circuit, the plurality of long codes,

respectively, by searching, in parallel, N/P chips, each long code of the plurality of long

codes.

51. The system as set forth in claim 50, with said transmitter further including

means for transmitting the plurality of long codes, at the plurality of phase angles.

52. The system as set forth in claim 51, with said acquisition further including

means for acquiring, from the communications channel, using the phase-acquisition circuit,

the plurality of long codes from the plurality of phase angles, respectively, of the carrier

signal by searching, in parallel, N/P chips, of each of the plurality of long codes.

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53. The system as set forth in claim 50 with said transmitter further including

means for transmitting the plurality of long codes at the plurality of phase angles,

respectively, on the carrier signal, with each phase angle in the plurality of phase angles

representing an M-ary phase scheme.

54. The system as set forth in claim 53, with said acquisition further including

means for acquiring, from the communications channel, using the phase-acquisition circuit,

the plurality of long codes, respectively, of the carrier signal by searching, in parallel, N/P

chips, of the first long code and the second long code.

55. The system as set forth in claim 50, with said generator further including

means for generating the plurality of ling codes, from a multiplicity of short codes, with each

short code different from other short code in the multiplicity of short codes, and each short

code in the multiplicity of short codes having a length less than N chips, with a first short

code thereby embedded in a first long code, and a second short code embedded in a second

long code.

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